

**mikroelektronik**

# **Information**

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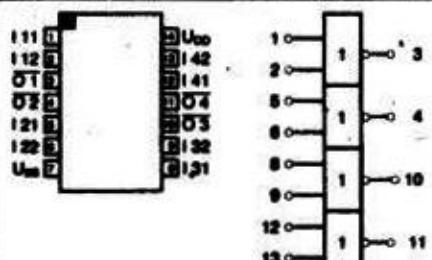


**Vergleichsliste CMOS-Schaltkreise**

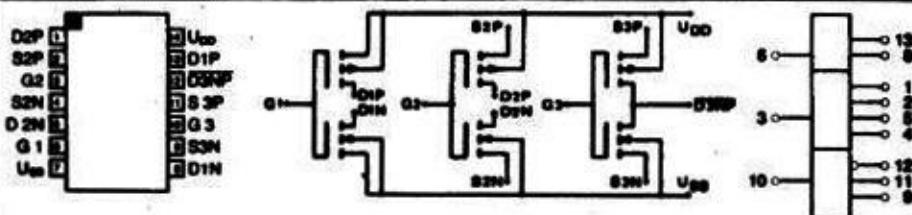
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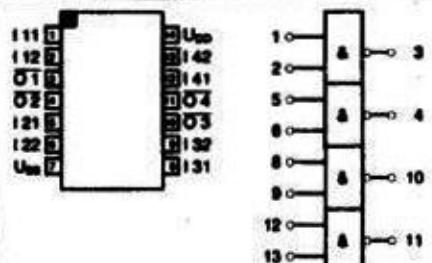
V 4001 D 4 NOR-Gatter mit je 2 Eingängen



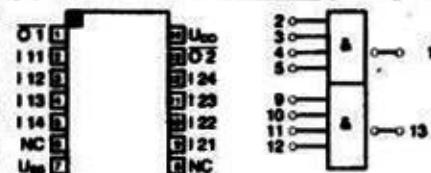
V 4007 D 2 Transistorpaare und 1 Inverter



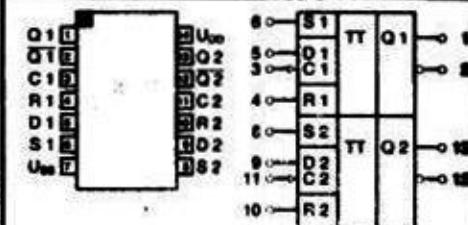
V 4011 D 4 NAND-Gatter mit je 2 Eingängen



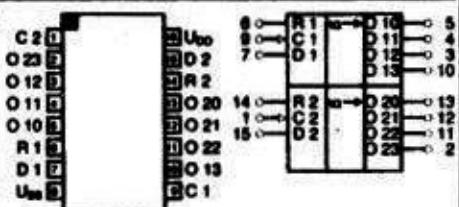
V 4012 D 2 NAND-Gatter mit je 4 Eingängen



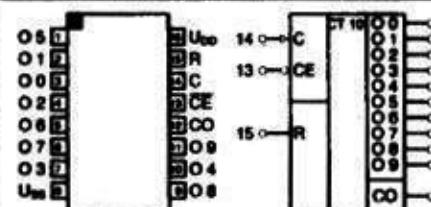
V 4013 D 2xD-Flip-Flop



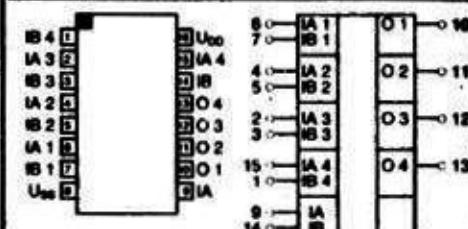
V 4016 D 2x4bit Schieberegister



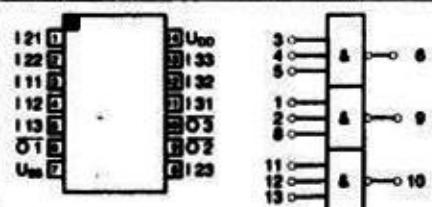
V 4017 D Dekadischer Zähler mit 10 dekodierten Ausgängen



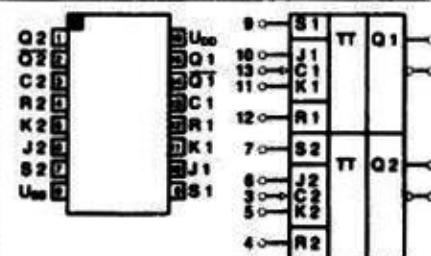
V 4019 D 4 AND/OR-Auswahlgatter



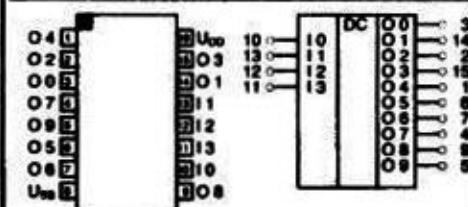
V 4023 D 3 NAND-Gatter mit je 3 Eingängen



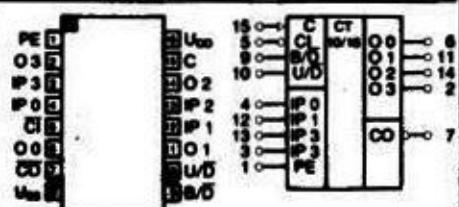
V 4027 D 2xJK-Flip-Flop



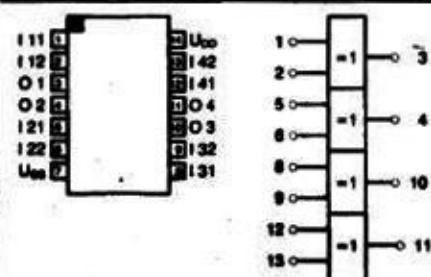
V 4028 D BCD/Decimal-Dekoder



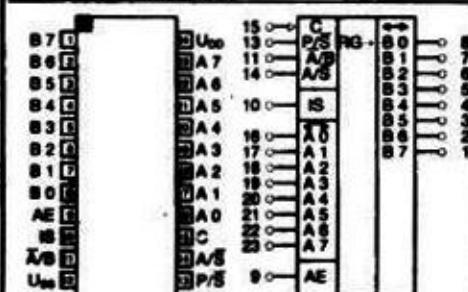
V 4029 D Synchroner 4stelliger binärer/BCD-Vor-/Rückwärtszähler

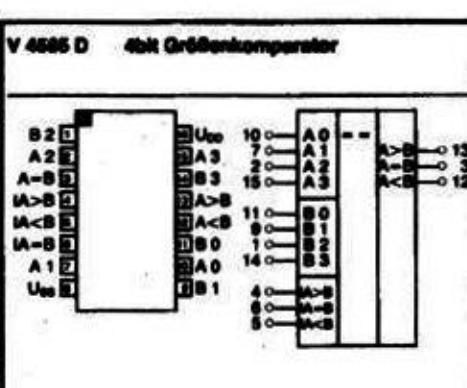
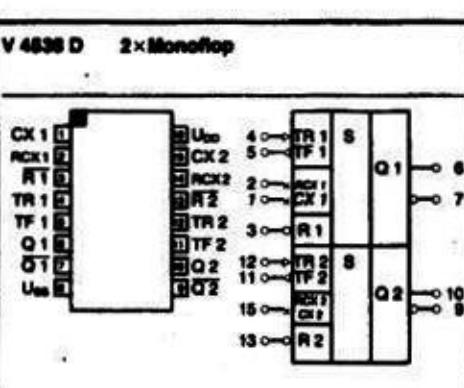
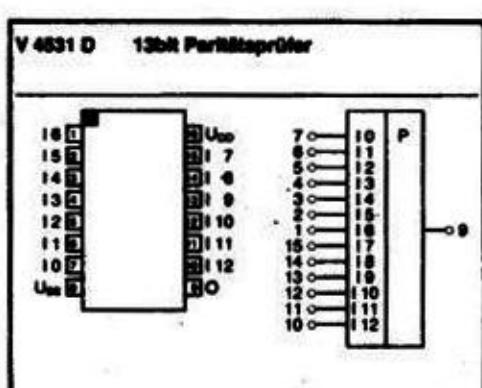
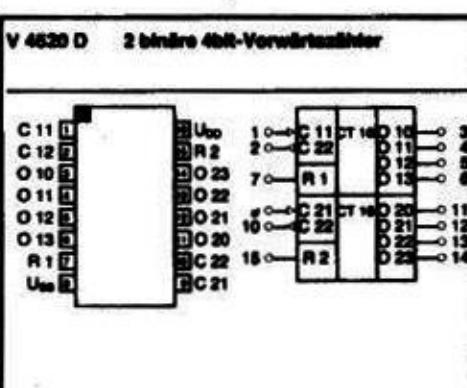
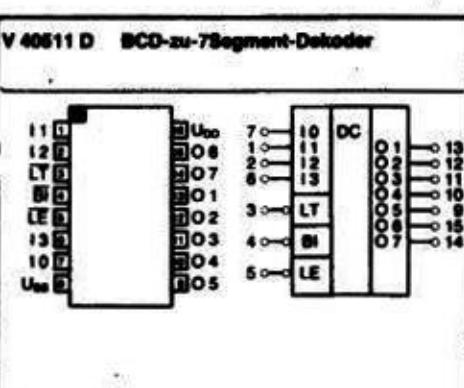
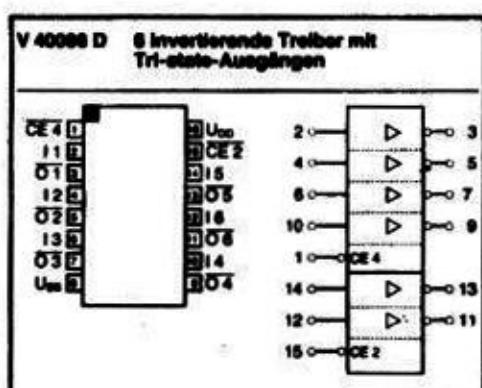
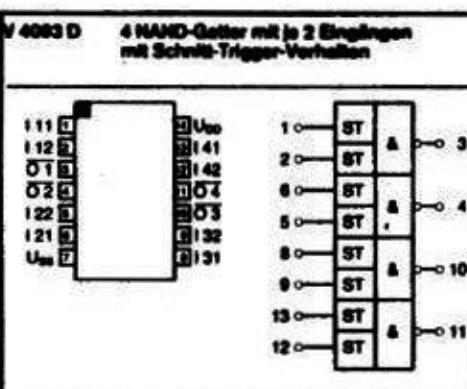
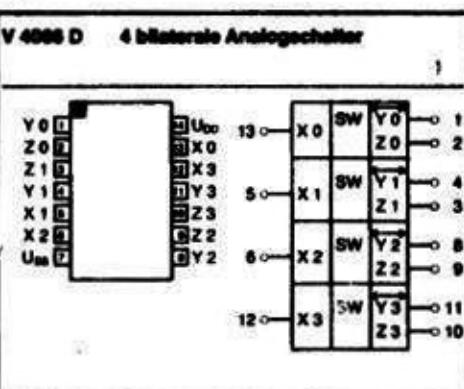
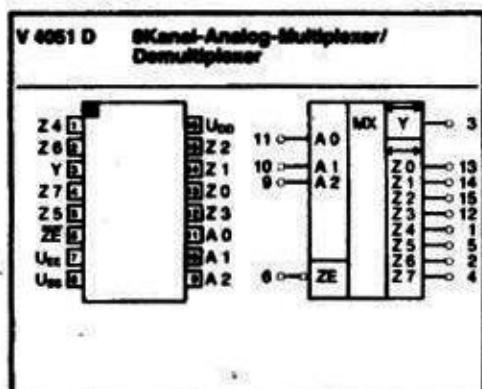
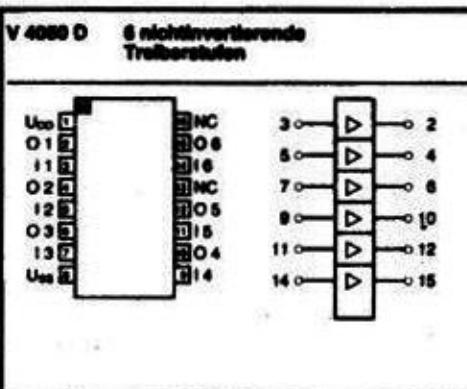
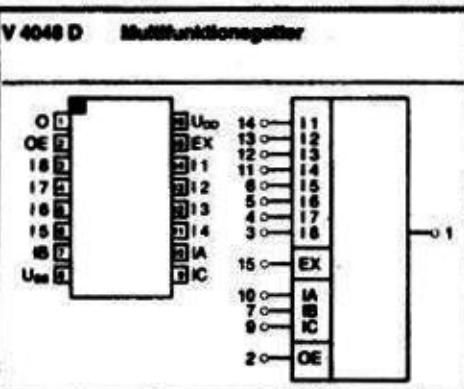
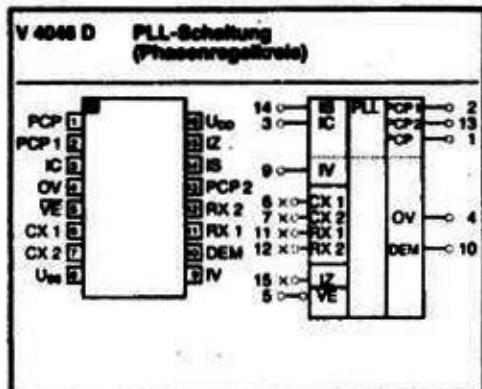
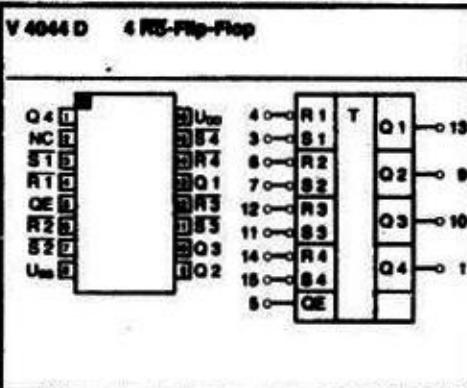
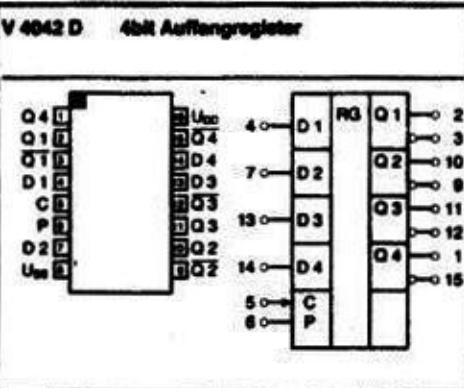
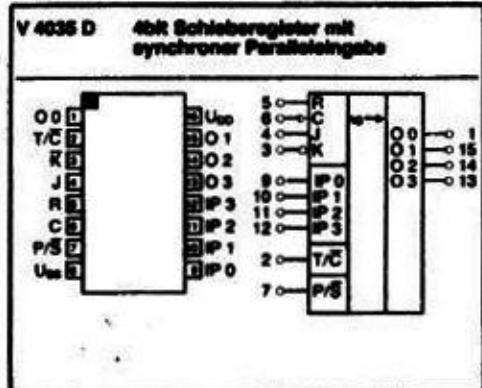


V 4030 D 4 Exklusiv-OR-Gatter mit je 2 Eingängen



V 4034 D Setzbares bidirektionales paralleles/serielles Speicherregister





Literatur

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CSSR, Tesla
- /29/ INTEGRATED CIRCUITS (DIGITAL) ELORG, UdSSR
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- /31/ Integralnye mikroschemy (zifrowye)
- /32/ Katalog integralnych mikroschem
- /33/ IC-Master 1982, USA
- /34/ IC-Master 1986, USA

Die vorliegenden Datenblätter dienen ausschließlich der Information! Es können daraus keine Lieferungs-Schaltungen oder Produktionsverbind-Nachlässe abgeleitet werden. Änderungen im Sinne des technischen Fortschritts sind vorbehalten.



Herausgeber

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